

HDMI 4x2 Matrix Switch RS-232 Command

1. RS-232 Command:

Baudrate: 19200
Data width: 8bit
Parity: none
Stop: 1bit

Port switch command package length is 13byte:

[0xa5+0x5b+0x02+0x03+**input port(1~4)**+0x00+**output port(1~2)**+0x00+0x00+0x00+0x00+0x00+**checksum**]

All you need to change is just "input port", "output port", "checksum"

Checksum = 0x100 - (0xa5+0x5b+0x02+0x03+**input port**+0x00+**output port**+0x00+0x00+0x00+0x00+0x00)

For example: Set output 1 form input 2 command:

A5 5B 02 03 02 00 01 00 00 00 00 00 F8

Output HDP status query package is 13byte:

This is a query command which mean you must send query package and then receive an answer.

For example: Query output 1(1~2) HPD status

Send package: A5 5B 01 05 **01** 00 00 00 00 00 00 00 F9

Receive package: A5 5B 01 05 01 00 **FF** 00 00 00 00 00 FA

The red **01** mean the output port number, it should be 1~2.

The blue **FF** mean this port's HPD is LOW, if **00** mean HIGH.

Input port status query package is 13byte:

This is a query command which mean you must send query package and then receive an answer.

[0xa5+0x5b+0x01+0x04+**output port(1~4)**+0x00+0x00+0x00+0x00+0x00+0x00+0x00+**checksum**]

For example: Query input 1(1~4) status

Send package: A5 5B 01 04 **01** 00 00 00 00 00 00 00 FA

Receive package: A5 5B 01 04 01 00 **FF** 00 00 00 00 00 FB

The red **01** mean the input port number, it should be 1~4.

The blue **FF** mean this port is plug in, if **00** mean plug out.

Beep on/off command package length is 13byte:

[0xa5+0x5b+0x06+0x01+**Beep onoff(0x0f:ON; 0xf0:OFF)**+0x00+0x00+0x00+0x00+0x00+0x00+0x00+**checksum**]

2. IR Code:

NEC CODE

#define SYSTEM_CODE 0x00

Function	Code
#define IR_KEY_TX_A_FROM_1	0x14
#define IR_KEY_TX_A_FROM_2	0x57
#define IR_KEY_TX_A_FROM_3	0x41
#define IR_KEY_TX_A_FROM_4	0x46
#define IR_KEY_TX_A_PRE	0x1d
#define IR_KEY_TX_A_NEXT	0x1f
#define IR_KEY_TX_B_FROM_1	0x19
#define IR_KEY_TX_B_FROM_2	0x1b
#define IR_KEY_TX_B_FROM_3	0x11
#define IR_KEY_TX_B_FROM_4	0x15
#define IR_KEY_TX_B_PRE	0x12
#define IR_KEY_TX_B_NEXT	0x59
#define IR_KEY_EDID_INDEX_1	0x5e
#define IR_KEY_EDID_INDEX_2	0x06
#define IR_KEY_EDID_INDEX_3	0x05
#define IR_KEY_EDID_INDEX_4	0x03
#define IR_KEY_EDID_INDEX_5	0x47
#define IR_KEY_EDID_INDEX_6	0x07
#define IR_KEY_EDID_INDEX_7	0x40
#define IR_KEY_EDID_INDEX_8	0x02
#define IR_KEY_EDID_INDEX_9	0x18
#define IR_KEY_EDID_INDEX_10	0x44
#define IR_KEY_EDID_INDEX_11	0x0f
#define IR_KEY_EDID_INDEX_12	0x51
#define IR_KEY_EDID_INDEX_OUT_A	0x0a
#define IR_KEY_EDID_INDEX_OUT_B	0x1e
#define IR_KEY_EDID_AUTO	0x0e
#define IR_KEY_INPUT_1	0x53
#define IR_KEY_INPUT_2	0x52
#define IR_KEY_INPUT_3	0x01
#define IR_KEY_INPUT_4	0x45
#define IR_KEY_INPUT_ALL	0x1a